

1/10
Crouse et al.
FIS920010331

Fig.1 (Prior Art)

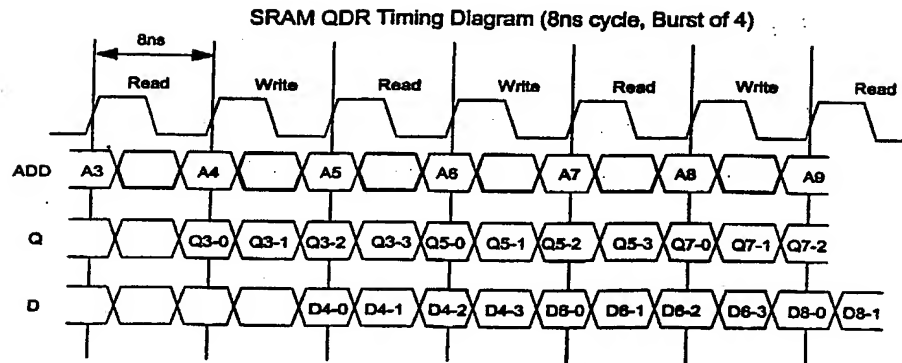
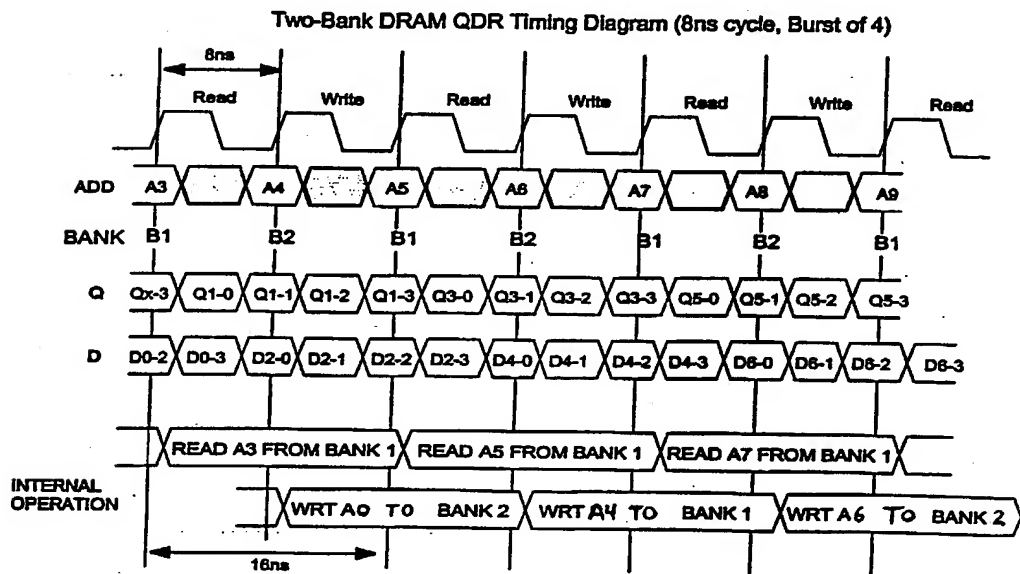


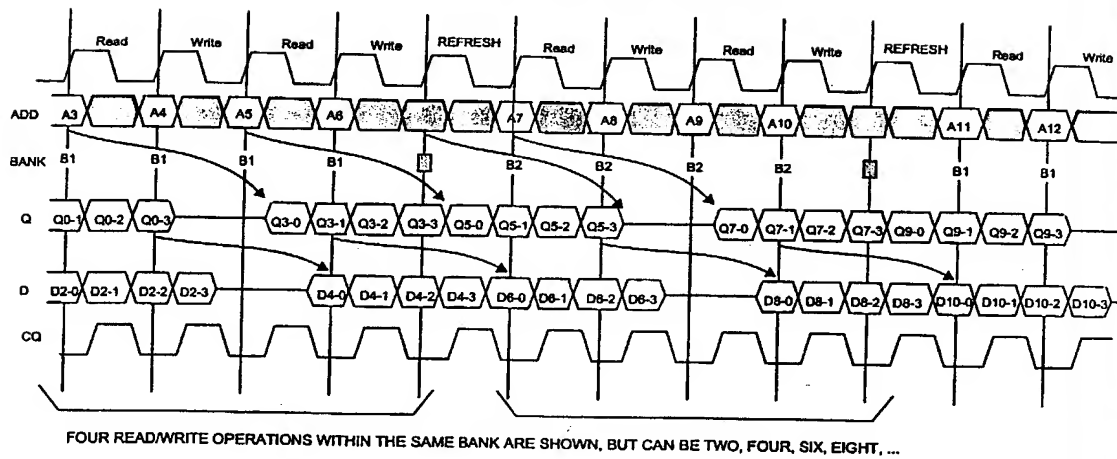
Fig.2 (Prior Art)



2/10
FIS920010331

Fig. 3

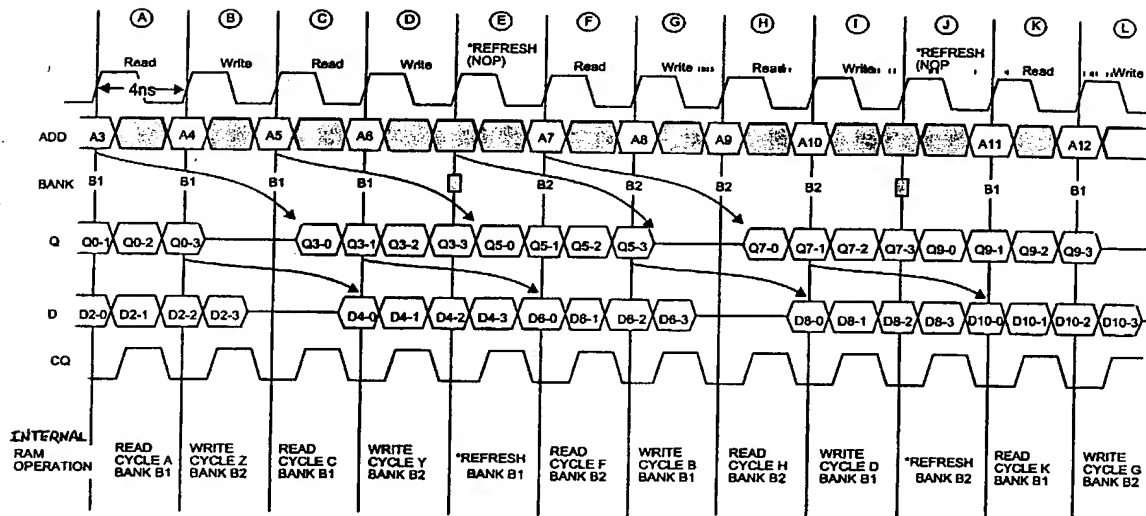
QDR 4-Burst 2-Bank Timing Diagram: Read-Write

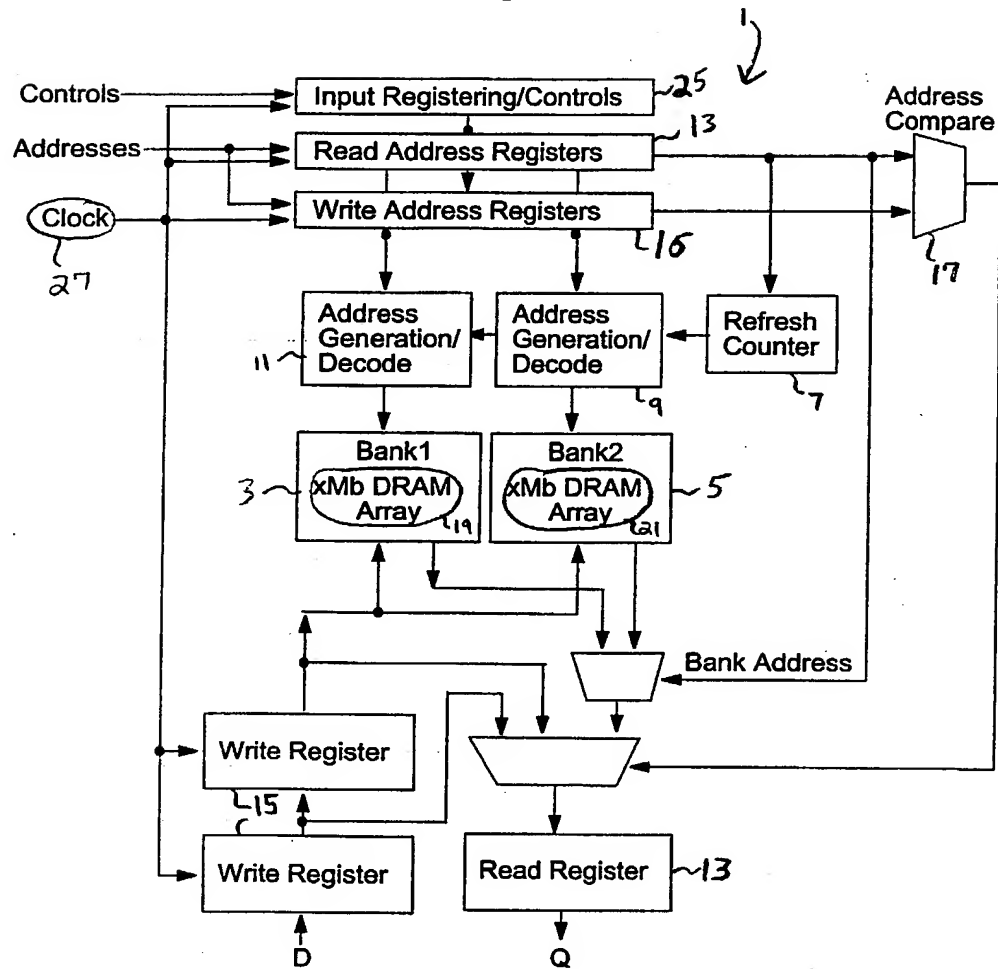


3/10
FIS920010331

Fig. 4

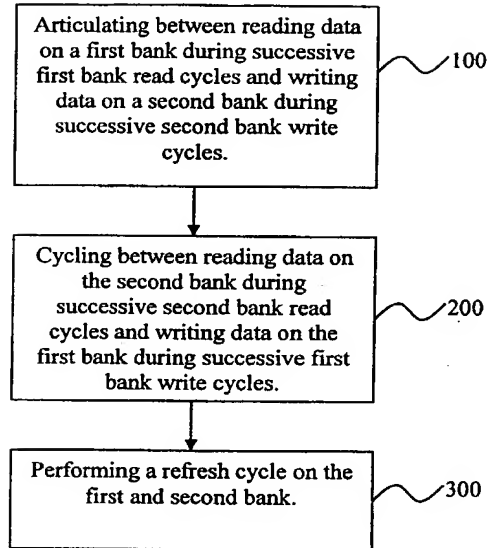
DETAILED TIMING WITH INTERNAL RAM OPERATION
QDR 4-Burst 2-Bank Timing Diagram: Read-Write



4/10
FIS920010331Fig. 5

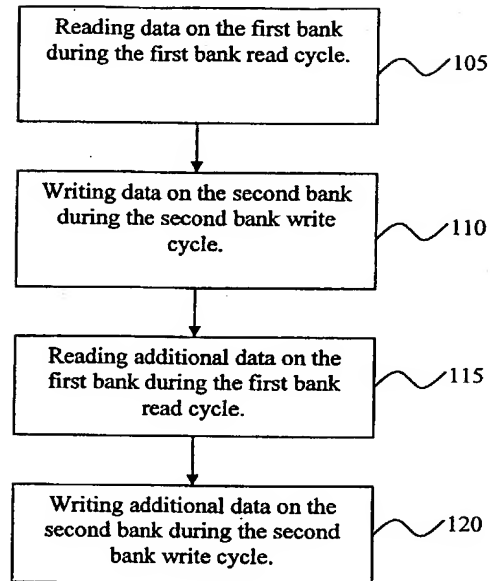
5/10
FIS920010331

Figure 6



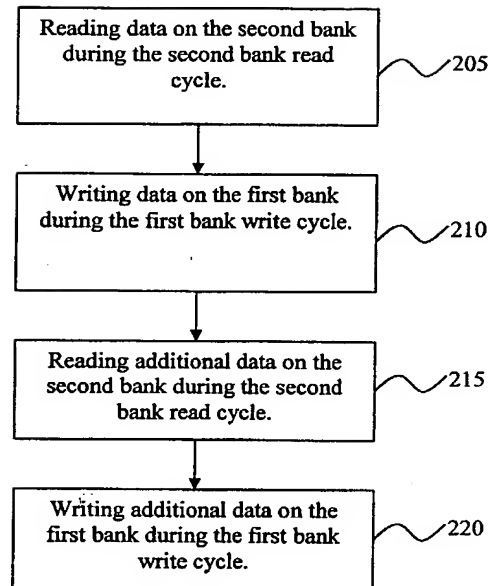
6/10
FIS920010331

Figure 7



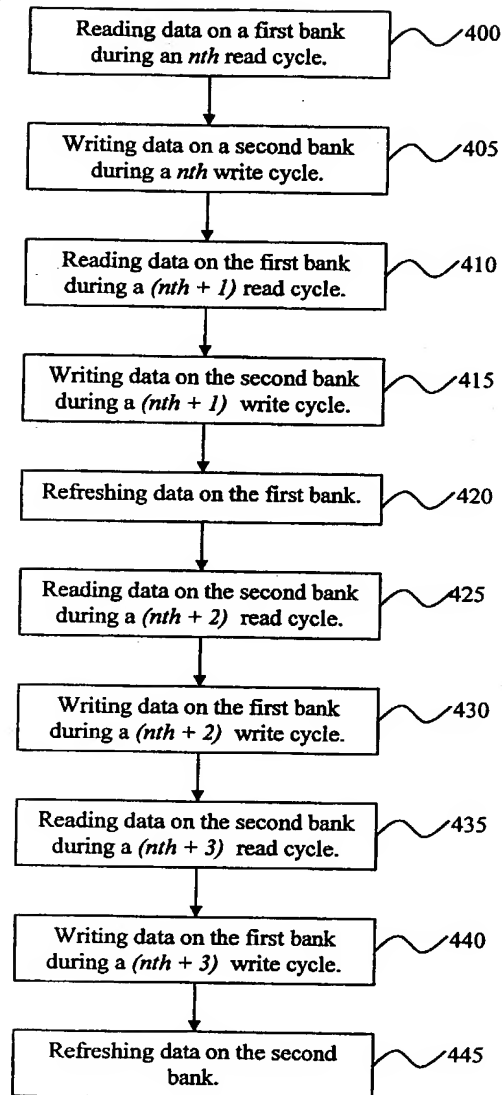
7/10
FIS920010331

Figure 8



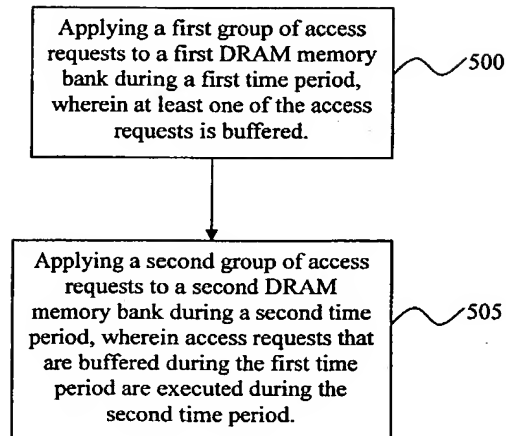
8/10
FIS920010331

Figure 9



9/10
FIS920010331

Figure 10



10/10
FIS920010331

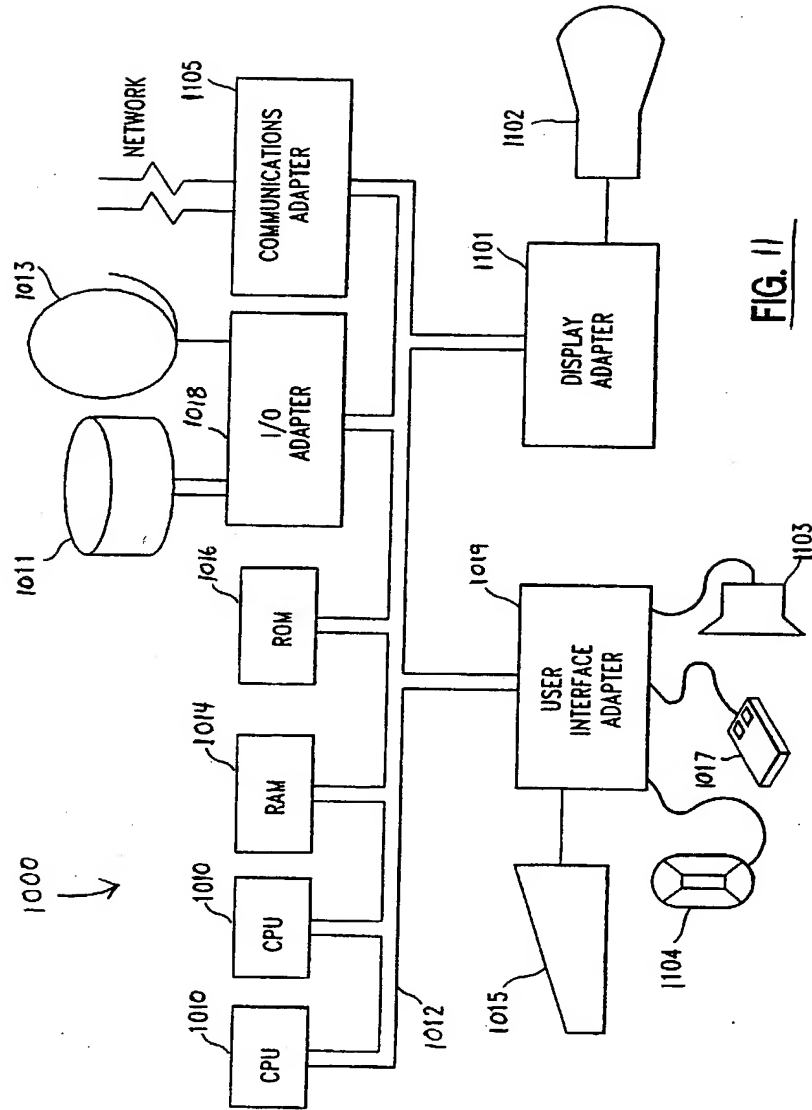


FIG. 11